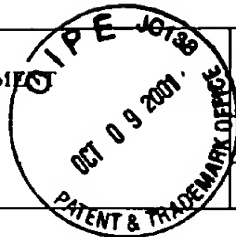


<b>FORM 1449*</b> <b>INFORMATION DISCLOSURE STATEMENT</b>  <b>IN AN APPLICATION</b> (Use several sheets if necessary)	Docket Number: 50037.8US01	Application Number: 09/882,076
	Applicant: Plante et al.	Filing Date: June 15, 2001


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## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

## FOREIGN PATENT DOCUMENTS

DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
					YES	NO

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

PC		Lorch et al., "Reducing Processor Power Consumption by Improving Processor Time Management in a Single-User Operating System", <i>Proceedings of the Second Annual International Conference on Mobile Computing and Networking</i> , Rye, NY, Nov 1996, pp. 143-154.
PC		Havinga et al., "Octopus: Embracing the Energy Efficiency of Handheld Multimedia Computers", <i>Proceedings of the Fifth Annual ACM/IEEE International Conference on Mobile Computing and Networking</i> , Seattle, WA, Aug 1999, pp. 77-87.
PC		Benini et al., "System-Level Power Optimization: Techniques and Tools", <i>Proceedings of the 1999 International Symposium on Low Power Electronics and Design</i> , San Diego, CA, Aug 1999, pp. 288-293.
PC		Wolfe, "Opportunities and Obstacles in Low-Power System-Level CAD", <i>Proceedings of the 33rd Annual Conference on Design Automation</i> , Las Vegas, NV, Jun 1996, pp. 15-20.
PC		Benini et al., "Monitoring System Activity for OS-Directed Dynamic Power Management", <i>Proceedings of the 1998 International Symposium on Low Power Electronics and Design</i> , Monterey, CA, Aug 1998, pp. 185-190.
PC		Benini et al., "Dynamic Power Management of Electronic Systems," <i>Proceedings of the 1998 IEEE/ACM International Conference on Computer-Aided Design</i> , San Jose, CA, 1998, pp. 696-702.
PC		Rudenko et al., "The Remote Processing Framework for Portable Computer Power Saving," <i>Proceedings of the 1999 ACM Symposium on Applied Computing</i> , San Antonio, TX, Feb/Mar 1999, pp. 365-372
PC		Bellas et al., "Using Dynamic Cache Management Techniques to Reduce Energy in a High-Performance Processor," <i>Proceedings of the 1999 International Symposium on Low Power Electronics and Design</i> , San Diego, CA, Aug 1999, pp. 64-69.



EXAMINER <u>P. Chandra</u>	DATE CONSIDERED <u>5/10/2004</u>
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form for next communication to the Applicant.